

# Patent Abstract of Japan

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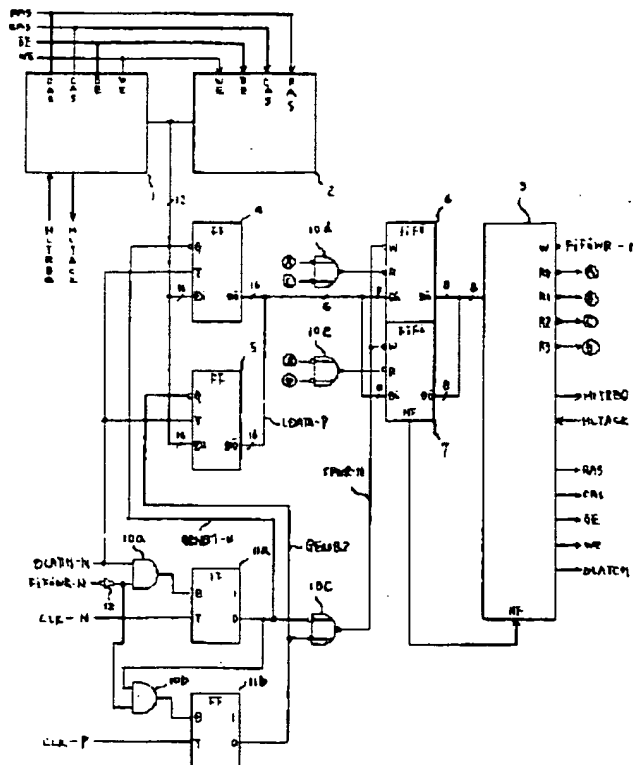
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TITLE : IMAGE DATA READING CIRCUIT OF  
PRINTER CONTROL DEVICE



**ABSTRACT :** PURPOSE: To provide the image data reading circuit of a printer control device wherein an FIFO memory is reduced by half.

**CONSTITUTION:** An image data reading circuit is equipped with the host receiving part performing the reception and transmission with a host CPU, a processor part 15 issuing a command for the host receiving part to execute the analysis and drawing of data, a drawing LSI part drawing data by the command from the processor part 15 and an engine control part performing the communication with an engine. By adding two-input AND gates 10a, 10b, one-bit flip-flops 11a, 11b, a two-input OR gate 10c and an inverter 12, writing data is divided to be written in FIFO memories 6, 7.

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